REW

Norney Docket No. <u>5646-42DVIP</u>

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

n re: Mario Au et al. Serial No. 10/612,849 Filed: July 3, 2003 Confirmation No. 6889 Group Art Unit: 2188 Examiner: Gary J. Portka

For:

FIFO MEMORY DEVICES HAVING MULTI-PORT CACHE MEMORY ARRAYS THEREIN THAT SUPPORT HIDDEN EDC LATENCY AND BUS MATCHING AND METHODS OF OPERATING SAME

December 16, 2005

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1451

AMENDMENT A

Sir:

This response addresses the Official Action of December 2, 2005.